

VERSION WITH MARKINGS TO SHOW CHANGES MADE

- **Marked up version of the tenth full paragraph at page 7, lines 20-23 through page 8, lines 1-11** (*which paragraph begins with "Referring now to FIG. 2," and ends with "executed during a boot-up sequence."*):

Referring now to FIG. 2, FIG. 2 is a block diagram of an example of a system-on-chip integrated circuit 70 that includes a touch screen interface circuit 100 in accordance with a preferred embodiment of the present invention. The integrated circuit 70 includes a plurality of devices that are disposed on a peripheral bus 72 including one or more universal asynchronous receiver-transmitters (UARTs) 73, one or more serial interfaces 74 for interfacing to external devices (such as digital to analog converters (DACs), audio controllers, and so on), interrupt controller/timers 75, a keypad interface 76, one or more I/O ports 77, and a touch screen interface circuit 100 (described in greater detail below). The integrated circuit 70 also includes a plurality of devices that are disposed on a processor bus 80 including one or more universal serial bus (USB) host interfaces 81 for connection to USB devices such as a keyboard, mouse, printer, and so on, an Ethernet port 82, DMA controllers 83, a microprocessor 86, a display interface 87 (for example, a raster engine), memory controllers 88 and 90, and boot ROM 89 for storing program code executed during a boot-up sequence.